

2818
#5
1-23-02^{cd}

PATENT
Attorney Docket No.: LEXA-00301

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Petro Estakhri *et al.*

Serial No.: 09/648,271

Filed: August 25, 2000

For: **FLASH MEMORY
ARCHITECTURE WITH
SEPARATE STORAGE OF
OVERHEAD AND USER DATA**

Group Art Unit: 2818

Examiner:

TRANSMITTAL LETTER

162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

RECEIVED
JAN 10 2003
TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed please find a Supplemental Information Disclosure Statement, a Certification pursuant to 37 C.F.R. § 1.97(e) for the Information Disclosure Statement, and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 1-6-03

By: Thomas B. Haverstock
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

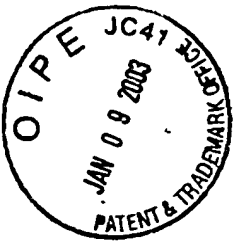
CERTIFICATE OF MAILING (37 CFR § 1.8(a)).

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

HAVERSTOCK & OWENS LLP.

Date: 1/6/03 By: [Signature]

RECEIVED
JAN 16 2003
Technology Center 2100



PATENT
Attorney Docket No.: LEXA-00301

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)

Petro Estakhri *et al.*)

Serial No.: 09/648,271)

Filed: August 25, 2000)

For: **FLASH MEMORY**)
ARCHITECTURE WITH)
SEPARATE STORAGE OF)
OVERHEAD AND USER DATA)

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are, therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. 1.56 and 1.97.

Applicant has become aware of the following printed publications which may be material to the examination of the instant application. The Examiner is requested to make these citations of official record in this application.

- U.S. Patent No. 5,737,742;
- U.S. Patent No. 5,835,935;
- U.S. Patent No. 5,838,614;
- U.S. Patent No. 5,905,993;
- U.S. Patent No. 5,907,856;
- U.S. Patent No. 5,946,714;
- U.S. Patent No. 6,076,137; and
- U.S. Patent No. 6,081,878.

Group Art Unit: 2818

Examiner:

SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT

162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

RECEIVED
JAN 10 2003
TECHNOLOGY CENTER 2800

RECEIVED
JAN 16 2003
Technology Center 2100

This Supplemental Information Disclosure Statement under 37 CFR 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination does not exist, or that any one or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 1-6-03

By: Thomas B. Haverstock
Thomas B. Haverstock
Reg. No. 32,571

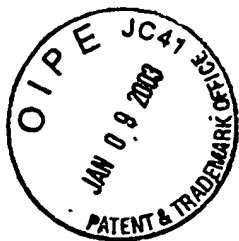
Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a));

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

HAVERSTOCK & OWENS LLP

Date: 1/6/03 By: File



PATENT
Attorney Docket No.: LEXA-00301

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Petro Estakhri *et al.*

Serial No.: 09/648,271

Filed: August 25, 2000

For: **FLASH MEMORY
ARCHITECTURE WITH
SEPARATE STORAGE OF
OVERHEAD AND USER DATA**

Group Art Unit: 2818

Examiner:

**CERTIFICATION FOR
SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT UNDER 37
CFR § 1.97(e)**

162 N. Wolfe Road
Sunnyvale, CA 94086
(408) 530-9700

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This certification is being made for the Supplemental Information Disclosure Statement accompanying this certification.

Certification

I, the person signing below, hereby certify that the printed publications contained in the enclosed supplemental information disclosure statement were cited within an Official Action mailed on December 2, 2002. **Accordingly, I hereby certify that no reference contained within this supplemental information disclosure statement was cited in a communication from a foreign patent office in a related foreign application, and, to the knowledge of the person signing the statement, after making reasonable inquiry, no reference contained within this supplemental information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of this supplemental information disclosure statement. 37 C.F.R. 1.97(e)(2).**

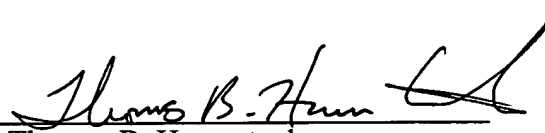
RECEIVED
JAN 16 2003
Technology Center 2100

Identification of Person Making Certification

The person making this certification is the attorney who signs below on the basis of the information in the attorney's file.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: 1-6-03

By: 
Thomas B. Haverstock
Reg. No.: 32,571

Attorneys for Applicants

CERTIFICATE OF MAILING (37 CFR § 1.8(a),

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

- 2 -

HAVERSTOCK & OWENS LLP.

Date: 1/6/03 By: 